

Logic Circuit

5

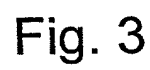
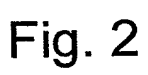


Fig. 3a

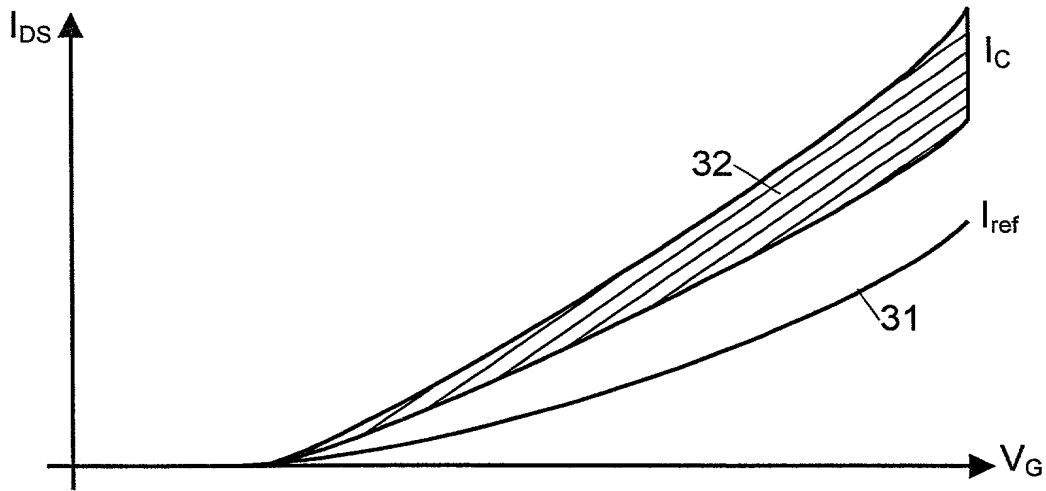


Fig. 4

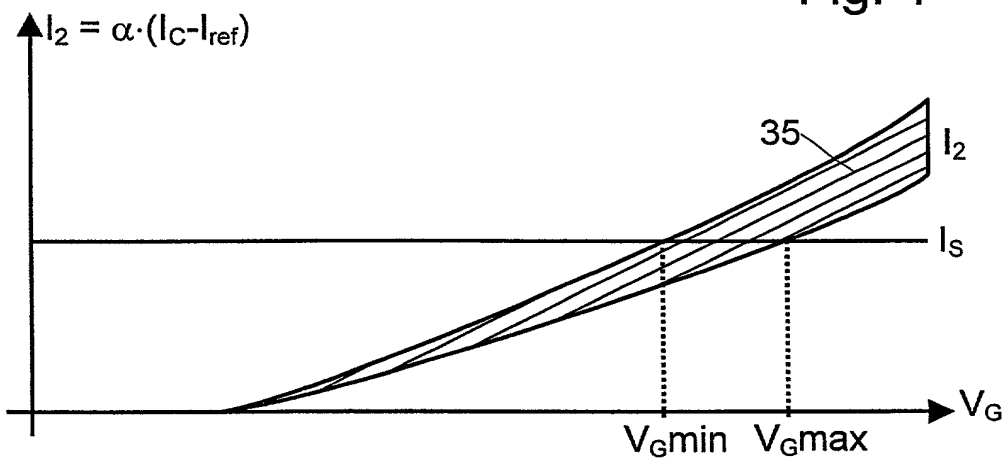


Fig. 5

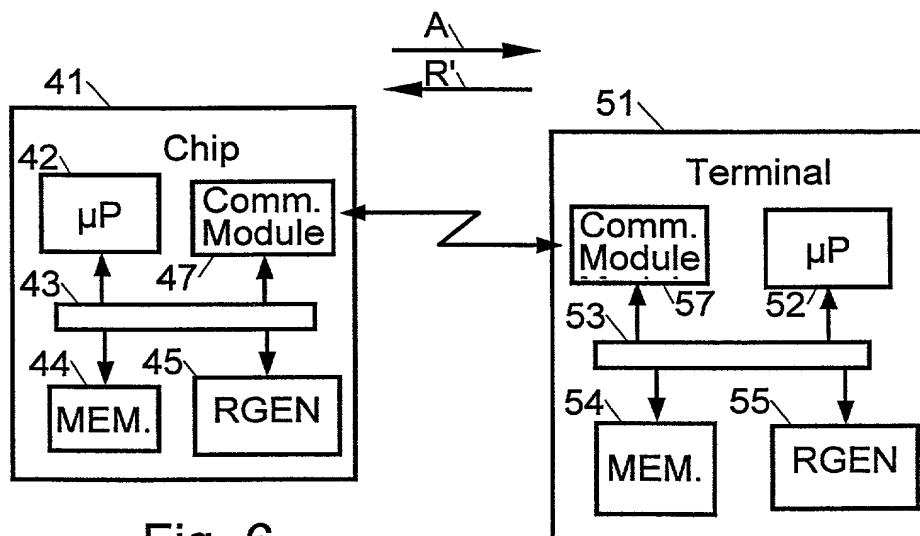


Fig. 6

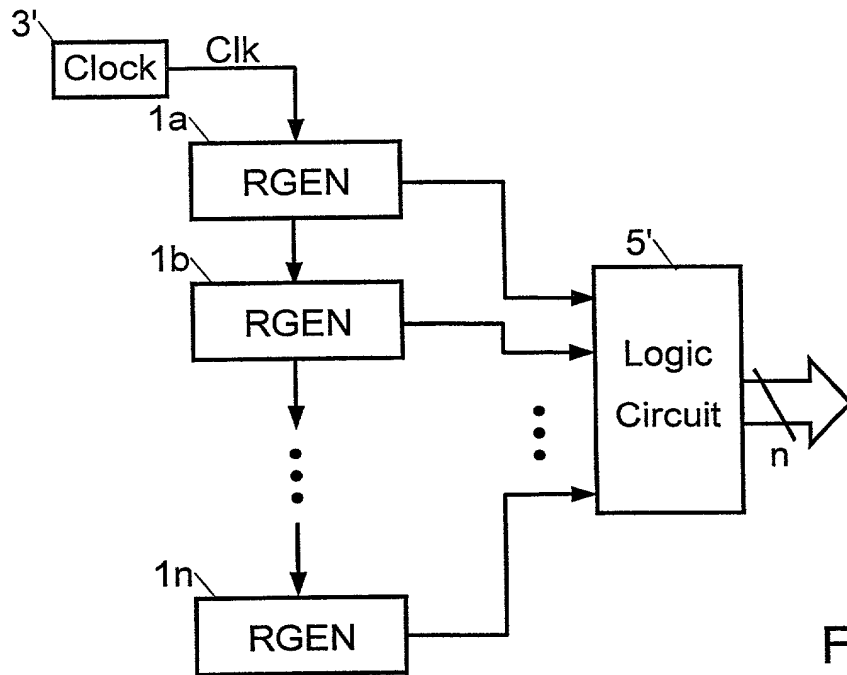


Fig. 7